

Docket No. 205263US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yasuhiko KURIYAMA

SERIAL NO: 09/817,216

GAU: 2817

FILED: March 27, 2001

EXAMINER: CHOE, H.

FOR: SEMICONDUCTOR POWER AMPLIFIER AND MULTISTAGE MONOLITHIC INTEGRATED CIRCUIT

REQUEST FOR EXTENSION OF TIME  
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

"RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2817"

SIR:

It is hereby requested that a **one** month extension of time be granted to July 26, 2003, for

☒ filing a response to the Official Action dated: March 26, 2003.

☐ responding to the requirements in the Notice of Allowability dated:

☐ filing the Formal Drawings. The Issue Fee due has been timely filed.

☐ responding to the Notice to File Missing Parts of Application dated:

☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.

☐ filing an Appeal Brief. A Notice of Appeal was filed on:

☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

08/05/2003 AWIS1 0000026 09817216

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110.00 OP

The required fee of \$110.00 is enclosed herewith by check and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

  
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(OSMMN 05/03)

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Christopher D. Ward

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JUL 25 2003  
TECHNOLOGY CENTER 2800